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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/327,282	06/04/99	JEONG	Y 8733D.6984

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EXAMINER

NGUYEN, K

ART UNIT

PAPER NUMBER

2674

DATE MAILED:

12/14/00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

# Office Action Summary

Application No.

09/327,282

Applicant(s)

JEONG ET AL.

Examiner

Kevin M. Nguyen

Art Unit

2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 04 June 1999.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-22 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-22 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claims \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. & 119(e).

## Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892)
- 16) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 17) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) g.
- 18) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 19) ☐ Notice of Informal Patent Application (PTO-152)
- 20) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Information Disclosure Statement***

1. The information disclosure statement filed 08/30/1999 and 02/29/2000. It has been placed in the application file, the information referred to therein has been considered as to the merits.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Ohwada et al. (5,442,370).

4. As to claim 1, Ohwada et al. teaches a method of driving a matrix type LCD which includes The display portion 1 is constituted by a transistor circuit consisting of TFT elements, liquid crystal, which is a displaying body, etc. The scanning side driving circuit 4 is a circuit for applying a scanning voltage to the scanning wiring 2, which consists of scanning electrodes connecting the gate electrodes of the TFT elements in the displaying portion 1. The signal wiring 3 intersects with the scanning wiring 2 and consists of signal electrodes, each of which is connected with each of the drain electrodes of the TFT elements (fig. 1, col. 2, lines 43-52).

5. As to claim 2, Ohwada et al. teaches a plurality of signal lines 3 are grouped in one set and the output of the signal side driving circuit 5 is applied to the display portion

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1 in accordance with the delay time by means of a plurality of the voltage-timing transforming circuit 7 for every set (col. 4, lines 46-51).

6. As to claims 3-6 and 19-21, Ohwada et al. teaches FIG. 7 shows an example of the concrete circuit construction of the timing generating circuit 8. This changes the width of the output pulse by regulating artificially the value of a capacitor  $C_{ix}$  and a resistor  $R_{ix}$  mounted externally on the basis of experiences by means of a well-known one shot pulse generating circuit 81. This pulse makes a monostable multivibrator 81 generate timing pulses  $V_{tg1}, \dots, V_{tg2}$  having a predetermined width in synchronism of the rise of this pulse. The transforming circuit 7 feeds the signal wiring with the signal voltage retarded by the delay times  $t_{d1}, t_{d2}, t_{d3}$  of this timing pulse (col. 5, lines 60-68).

7. As to claims 7-9, 16-18 and 22, Ohwada et al. teaches an apparatus which includes plurality of signal lines 3 are grouped in one set and the output of the signal side driving circuit 5 is applied to the display portion 1 in accordance with the delay time by means of a plurality of the voltage-timing transforming circuit 7 for every set (col. 4, lines 46-51).

8. As to claims 10-15, Ohwada et al. teaches the transforming circuit 7 and the timing generating circuit 8 constitute signal delaying means (col. 2, lines 63-65).

### ***Conclusion***

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO-892 form.

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Nguyen whose telephone number is 703-305-

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6209. The examiner can normally be reached on M-F (9:00-5:00), with alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on 703-305-4709. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-6606 for regular communications and 703-308-6606 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Kevin M. Nguyen  
Examiner  
Art Unit 2674

KN  
December 11, 2000



**RICHARD HJERPE**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2600**